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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

**INFORMATION DISCLOSURE
STATEMENT**

Docket Number:
2885/21

Application Number
09/335,974

Filing Date
June 18, 1999

Examiner

Art Unit
2751

Invention Title
I/O AND MEMORY BUS SYSTEM FOR DFPS AND
UNITS WITH TWO- OR MULTI-DIMENSIONAL
PROGRAMMABLE CELL ARCHITECTURES

Inventor(s)
Martin VORBACH et al.

Assistant Commissioner for Patents
Washington D.C. 20231
Box Patent Application

I hereby certify that this correspondence is being deposited with the
United States Postal Service as first class mail in an envelope addressed
to: Assistant Commissioner for Patents, Washington, D.C. 20231 on

Date: 17 sept 1999

Reg. No. 36,098

Signature: _____

Michelle M. Carniaux

- I. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the references listed on the attached modified PTO Form No. 1449 to the attention of the Examiner. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
- II. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated.

Dated: 17 sept 1999

By: _____

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